

**ABSTRACT**

A display pixel circuit having a data line loading circuit with a first capacitor, a first reset circuit coupled with the data line loading circuit, and a diode coupled to the data line loading circuit. The display pixel circuit also includes a second capacitor associated with the diode, and a second reset circuit coupled with the second capacitor. The first reset circuit is configured to reset the first capacitor. The second reset circuit is configured to reset the second capacitor, and the diode is driven by a constant current after resetting the first capacitor and the second capacitor.